

PATENT NUMBER \_\_\_\_\_

ISSUE CLASSIFICATION	
Class	Subclass

**O.I.P.E.**

**PATENT DATE**

1222  
SCANNED

**Q.A**

APPLICATION NO. 09/746815	CONT/PRIOR	CLASS 711	SUBCLASS <del>101</del> 102	ART UNIT 2485	EXAMINER <del>11</del> B. Kersch
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## APPLICANTS

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### FILE

Multiple memory aliasing for a configurable system-on-chip

PTO-2040  
12/99[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
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<b>ISSUE FEE</b>				
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Primary Examiner) _____ (Date)		Amount Due	Date Paid
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